


Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1427		SERIAL NO. 09/559,903		
 LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Zhiping Yin et al.				
				FILING DATE April 26, 2000		GROUP 2815		
U.S. PATENT DOCUMENTS								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
YJS	AA	6,001,747	12-1999	Annappagada	430	790		
	AB							
	AC							
	AD							
	AE							
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	AI							
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	AL							
FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AM							
	AN							
	AO							
	AP							
	AQ							
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)								
YJS	AR		S. Wolf and R.M. Tauber, Silicon Processing for the VLSI Era (Vol 1-Process Technology), pages 189-190, 1986.					
	AS							
	AT							
EXAMINER				DATE CONSIDERED				
YJS				11/3/03				
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.								

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